

Towards a Coq-Verified Compiler from Esterel to Circuits

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Long-term goal

Follow the ideas of CompCert:

- Esterel kernel is simpler than C
 \leadsto I only consider Pure Esterel v5
- Coq allows extracting the compiler
- I could also go to C code and link with CompCert
- several transformation passes
 \leadsto semantics is preserved across passes

Here, only the formalization of several semantics of Esterel
(it's just the beginning!)

High-level view of semantics

A **reaction** $P \longrightarrow^* P'$ of P into P' in several ticks

One **reduction** $p \rightarrow \dots \rightarrow q$ for each clock tick, having:

- inputs I
- outputs O
- return code k

I only consider reductions, written $p \xrightarrow[I]{O,k} p'$

emit s; pause; present i then pause else emit s $\xrightarrow[I]{\{s\}, 1}$

present i then pause else emit s $\xrightarrow[I]{\{s\}, 0}$

nothing

Pure Esterel kernel syntax (commands)

$p, q :=$	0	nothing	
	1	pause	
	$T + 2$	exit T	T a de Bruijn integer
	!s	emit s	
	$s?p \cdot q$	present s then p else q end	
	$s \supset p$	suspend p when s	
	$p; q$		
	$p \parallel q$		
	p^*	loop p end	
	$\uparrow p$		does not exist in the language
	{p}	trap p end	
	$p \setminus s$	signal s in p end	

+ macros, ex: $s \supset p := \{(s?1 \cdot 2)^*\}; s \supset p$

Classical Behavioral Semantics

Notation for reductions: $p \xrightarrow[l]{O,k} p'$

- LBS (Logical Behavioral Semantics) [Gonthier]
 \rightsquigarrow the reference semantics
- DS (Deterministic Semantics): [Tardieu]
 avoids non-determinism in $p \setminus s$
 \rightsquigarrow same value of s in both branches
- RDS (Reactive Deterministic Semantics): [Tardieu]
 adds error cases ($k \in \mathbb{N} \cup \{\infty\}$)
 \rightsquigarrow instantaneous loop looperror
 $\rightsquigarrow p \setminus s$: different values de s dans p signalMP / signalPM
 \rightsquigarrow + propagation (only $p \setminus s$) signal ∞

Difference for signal with s present

$$\frac{p \xrightarrow{O_+, k_+} p' \quad s \in O_+}{p \setminus s \xrightarrow{O_+ \setminus \{s\}, k_+} p' \setminus s} \text{LBS}$$

$$\frac{p \xrightarrow{O_+, k_+} p' \quad p \xrightarrow{O_-, k_-} p' \quad s \in O_+, O_-}{p \setminus s \xrightarrow{O_+ \setminus \{s\}, k_+} p' \setminus s} \text{DS}$$

$$\frac{p \xrightarrow{O_+, k_+} p' \quad p \xrightarrow{O_-, k_-} p' \quad s \in O_+, O_- \quad k_+, k_- < \infty}{p \setminus s \xrightarrow{O \setminus \{s\}, k_+} p' \setminus s} \text{RDS}$$

Properties

	deterministic	reactive (i.e. total)
LBS	X	X
DS	✓	X
RDS	✓	✓

~> RDS can be a function

- If $k < \infty$, $RDS \iff DS \implies LBS$

- In $p \xrightarrow[l]{O,k} p'$, if $k \neq 1$, then $p' = \text{nothing}$.

~> requires to add δ

$\delta k p :=$ if $k = 1$ then p else nothing

- $p \xrightarrow[l]{O,\infty}_{RDS} p' \iff$ we use looperror or signalMP/signalPM

~> requires to **formally** define “use”

~> (p, l) **error-free** := they are not used in the reduction

What about Coq?

All results in this talk are proved in Coq

Feedback from using Coq:

- I, O = events or sets of signals?
 \leadsto Gérard Berry vs. Olivier Tardieu
- higher-order logic: one definition for determinism

Definition deterministic $\{T : \text{Type}\} (R : \text{semantics } T) p :=$
 $\text{forall } I \ O_1 \ k_1 \ p_1 \ O_2 \ k_2 \ p_2, R \ p \ I \ O_1 \ k_1 \ p_1 \rightarrow R \ p \ I \ O_2 \ k_2 \ p_2 \rightarrow$
 $S.\text{Equal } O_1 O_2 \wedge k_1 = k_2 \wedge p_1 = p_2.$

- compatibility w.r.t. set equality on I and O

- find bugs: $p \xrightarrow{O, \infty}_{RDS} 0 \Rightarrow O = \emptyset$ $!s \parallel 0^*$ [Tardieu]

\leadsto should we change the semantics?

Why a constructive semantics?

- remove all problems of:
 - non-determinism
 - backward dependency across sequence

- closer to circuit semantics

[Berry, Mendler, Shiple]

constructive circuit = stabilizes for all delays

- based on what **Must/Can** be done

\rightsquigarrow only affects $p \setminus s$

- s true in $p \iff s \in \text{Must}(p, E)$
- s false in $p \iff s \notin \text{Can}(p, E)$
- otherwise, we block!

The Constructive Case

CBS = Constructive Behavioral Semantics

cf. Esterel Constructive Book

Must: what must be done

- Must_S : set of signals that must be emitted
- Must_k : the return code that p must return (at most 1)

Can: what can be done

- Can_S : set of signals that can be emitted
- Can_k : set of return codes that p can return

Again I, O : events or sets?

\rightsquigarrow sets but also requires A , set of **absent signals**

Notation:

$$p \xrightarrow[A, I]{O, k}_{\text{CBS}} p'$$

Properties of Can/Must

- Max $K L$ defined by Gonthier's formula

$$\begin{aligned} \text{Max } K L &= \{n \in \mathbb{N} \mid \exists k l, k \in K \wedge l \in L \wedge n = \max k l\} \\ &= \{n \in K \cup L \mid n \geq \min K \wedge n \geq \min L\} \end{aligned}$$

+ its specification:

$$\begin{aligned} \text{Max } K L &:= \text{filter}(\geq (\min L)) (\text{filter}(\geq (\min K)) (K \cup L)) \\ \text{with } x \in (\text{Max } K L) &\iff \exists k l, k \in K \wedge l \in L \wedge x = \max k l \end{aligned}$$

- $\text{Must } p E \subseteq \text{Can}^+ p E$ (separate proof)
- monotony of Can and Must
 \rightsquigarrow big mutual induction: 400 l. of proof!

Properties of CBS

- Deterministic but not reactive
 - ~ Must/Can may get stuck
 - ~ we can use ∞ to track errors
- In $p \xrightarrow[A,I]{O,k} p'$, if $k \neq 1$, then $p' = \text{nothing}$ (requires some δ)
- CBS et Can/Must: if $p \xrightarrow[A,I]{O,k} p'$, then
 - $s \in \text{Must}_s p E_{A,I} \implies s \in O$ + idem for k
 - $s \in O \implies s \in \text{Can}_s^m p E_{A,I}$ + idem for k
- Same for LBS (mutually recursive proof of 200 l.)
- CBS \implies LBS mais CBS $\not\Rightarrow$ RDS
- If p error-free, CBS \implies RDS
 - ~ CBS ignores errors inside unreachable code ($p \setminus s$)

Why a state semantics?

Intermediate step between Esterel and circuits:

- on source code but the program does not change (unlike LBS)
- program counters indicate where we are
- state semantics very close to circuit semantics
- pause statements are mapped to registers

\downarrow emit s; pause; present i then pause else emit s $\xrightarrow[\downarrow]{\{s\}, 1}$
 emit s; pause; \downarrow present i then pause else emit s $\xrightarrow[\downarrow]{\{s\}, 0}$
 emit s; pause; present i then pause else emit s \downarrow

Commands and states

state = command being evaluated

$$\hat{p}, \hat{q} := \begin{array}{l} 0 \\ 1 \\ T + 2 \\ !s \\ s?p \cdot q \\ s \supset p \\ p; q \\ p \parallel q \\ p^* \\ \uparrow p \\ \{p\} \\ p \setminus s \end{array}$$

Commands and states

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$$\hat{p}, \hat{q} := \left| \begin{array}{l} 1 \\ \\ s?p \cdot q \\ s \supset p \\ p; q \\ p \parallel q \\ p^* \\ \uparrow p \\ \{p\} \\ p \setminus s \end{array} \right.$$

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Commands and states

state = command being evaluated

$$\hat{p}, \hat{q} := \left| \begin{array}{l} \hat{1} \quad \text{activated pause} \\ \\ s? \hat{p} \cdot q \mid s?p \cdot \hat{q} \\ s \supset \hat{p} \\ \hat{p}; q \mid p; \hat{q} \\ \hat{p} \parallel q \mid p \parallel \hat{q} \mid \hat{p} \parallel \hat{q} \\ \hat{p}^* \\ \uparrow \hat{p} \\ \{\hat{p}\} \\ \hat{p} \setminus s \end{array} \right.$$

Commands and states

state = command being evaluated

$\hat{p}, \hat{q} :=$	$\hat{1}$	activated pause	
	$s? \hat{p} \cdot q \mid s?p \cdot \hat{q}$		$\hat{p} = \text{state}$ (computation pending)
	$s \supset \hat{p}$		
	$\hat{p}; q \mid p; \hat{q}$		$p = \text{command}$ (computation done)
	$\hat{p} \parallel q \mid p \parallel \hat{q} \mid \hat{p} \parallel \hat{q}$		
	\hat{p}^*		$\bar{p} := \hat{p} \mid p$ (term)
	$\uparrow \hat{p}$		
	$\{\hat{p}\}$		
	$\hat{p} \setminus s$		

State Semantics

LSBS = Logical State Behavioral Semantics

- 2 types of rules: s-rules (start) and r-rules (resume)
 - \leadsto 2 distinct inductive definitions:
 - first sLSBS from commands to terms
 - then rLSBS from states to terms
 - \leadsto LSBS = sLSBS \cup rLSBS from terms to terms
- main theorem:

$$\hat{p} \xrightarrow[l]{O,k}_{\text{rLSBS}} \overline{p'} \implies \exists q, \mathcal{E}(\hat{p}) \xrightarrow[l]{O,k}_{\text{LSB}} q \wedge \delta k(\mathcal{E}(\overline{p'})) \equiv q$$

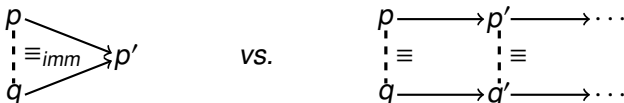
where $p \equiv q :=$ “ p and q equivalent”

$\mathcal{E}(\hat{p}) :=$ expansion of \hat{p}

= what will be executed in the next tick

Bugs corrected

- $p \equiv q$: immediate equivalence vs. bisimulation



- \equiv_{imm} not compatible with the kernel:

$$p \equiv_{imm} q \not\Rightarrow s \supset p \equiv_{imm} s \supset q \quad (\text{when } s \text{ is present})$$

- bisimulation requires coinduction

- small mistake in the definition of states:

$\bar{p} \parallel \bar{q}$ allows two commands ($p \parallel q$)

- $\hat{p} \xrightarrow[l]{O, k}_{rLSBS} p' \implies (k = 1 \iff p' \text{ state})$

\rightsquigarrow was wrong for $p \parallel q$ (when $k_p = 1$ and $k_q > 1$)

Constructive State Behavioral Semantics

Yet another semantics: CSBS

- Same restriction on LSBS as going from LBS to CBS (signal declaration must be constructive)
 \leadsto Must/Can extended to states
- proofs work exactly the same

Conclusion & Next Steps

Summary:

- formal proofs of “obvious results” (except the wrong ones)
- iron out a few bugs
- only semantics here
 - ↪ necessary to compare Esterel and circuits

Next steps:

- get to circuits!
 - ↪ write their semantics
 - ↪ (finally) write the compiler
- prove that compilation preserve semantics
- handle schizophrenia & verify optimization
- toward full Esterel?

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Thank you
for your attention